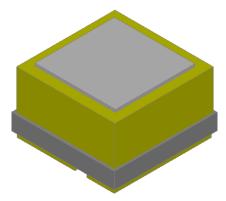
JSFON



OC-VC series

Product Specification

| Product | 3535 VCSEL Emitter |
|-------------|---------------------|
| Part Number | OCVC35-1290V9420-V6 |
| Issue Date | 2019/10/14 |



Features

- Vertical Cavity Surface Emitting Laser technology
- Package size = 3.5mm x 3.5mm x 1.6mm
- Pulse Output power 2W emitter
- Peak wavelength λ_p =940nm
- Divergence angles= 120°x90°
- Wavelength stabilized (0.07nm per °C)
- Narrow spectral width (<1nm)
- RoHS

122

Applications

- 3D depth sensing
- Gesture sensing
- Flood illuminator



SEONI

Product Nomenclature

The product name is designated as below:

<u>OCVCAB</u> – <u>CDEFGHIJK</u>

Designation:

OCVC = Orient-Chip Technology Co., LTD VCSEL Series Product. AB = Package size $_{(1)}$ CDEF = Divergence Angle $_{(2)}$ G = Internal Code HI = Wavelength $_{(3)}$ JK = Pulse mode Output Power $_{(4)}$

Notes

| 1. Package | e Size: |
|------------|---------------|
| Symbol | Description |
| 35 | 3.5mm x 3.5mm |

2. Divergence Angle:

| Symbol Description 4436 44° x 36° 5040 50° x 40° 6045 60° x 45° 7258 72° x 58° | y |
|--|----------|
| 5040 50° x 40° 6045 60° x 45° 7258 72° x 58° | Symbol |
| 6045 60° x 45° 7258 72° x 58° | 4436 |
| 7258 72° x 58° | 5040 |
| | 6045 |
| | 7258 |
| 9070 90° x 70° | 9070 |
| 1190 110° x 90° | 1190 |
| 1290 120° x 90° | 1290 |

3. Wavelength:

| Symbol | Description |
|--------|-------------|
| 81 | 810nm |
| 85 | 850nm |
| 94 | 940nm |

4. Pulse mode Output Power:

| Symbol | Description |
|--------|-------------|
| 05 | 0.5W |
| 10 | 1W |
| 20 | 2W |
| 30 | 3W |
| 40 | 4W |



Absolute Maximum Ratings

| Parameter | Symbol | Values | Unit |
|--|------------------|--------------------|------|
| Operating temperature range | T _c | -20 ~ 85 | °C |
| Storage temperature range | T _{stg} | -40 ~ 85 | °C |
| Maximum package SMT solder Reflow temperature | | 260°C 10seconds | °C |

<u>Characteristics(Ta = 25 °C)</u>

| Parameter | Symbol | Condition | Min. | Тур. | Max. | Unit |
|--------------------------------|---------------------------|------------------------|------|------|------|-------|
| Pulse Output power | Po | I _F = 3.0 A | 1900 | 2400 | - | mW |
| Wavelength | λ_{p} | I _F = 3.0 A | 930 | 940 | 950 | nm |
| Forward Voltage | Vf | I _F = 3.0 A | 1.7 | 2.0 | 2.3 | V |
| Threshold current | I _{th} | | | 500 | - | mA |
| Power Conversion Efficiency | PCE | I _F = 3.0 A | - | 34 | - | % |
| Wave Shift | $\Delta \lambda \Delta T$ | I _F = 3.0 A | - | 0.07 | - | nm/°C |
| Slope Efficiency | | | - | 0.96 | - | W/A |

Note:

- Forward Voltage tolerance is ±0.1 V, Radiant Intensity tolerance is ±10% Others measurement allowance is ±10%.
- 2. VCSEL is mounted on PCB and measured with operating bias current @ 3A,10ms
- 3. Due to the special conditions of the manufacturing processes of VCSEL, the typical data of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.



Radiometric Power Bin Structure at 3000mA

| Color | Bin Code | Minimum Radiometric Power (mW) | Typical Radiometric Power (mW) | | | |
|--|-----------|-----------------------------------|-----------------------------------|--|--|--|
| ared 940 | A2 | 1900 | 2400 | | | |
| OCS maintains a tolerance of ±10% on flux and power measurements. The flux bin of the product may be modified for improvement without notice. | | | | | | |
| | | gth Bin Structure at | <u>3000mA</u> | | | |
| Color | Bin Code | Centroid Wavelength | | | | |
| Infrared 940 94 940 | | | | | | |
| ∎ Forw | ard Volta | ge Bin Structure at 3 | 8000mA | | | |

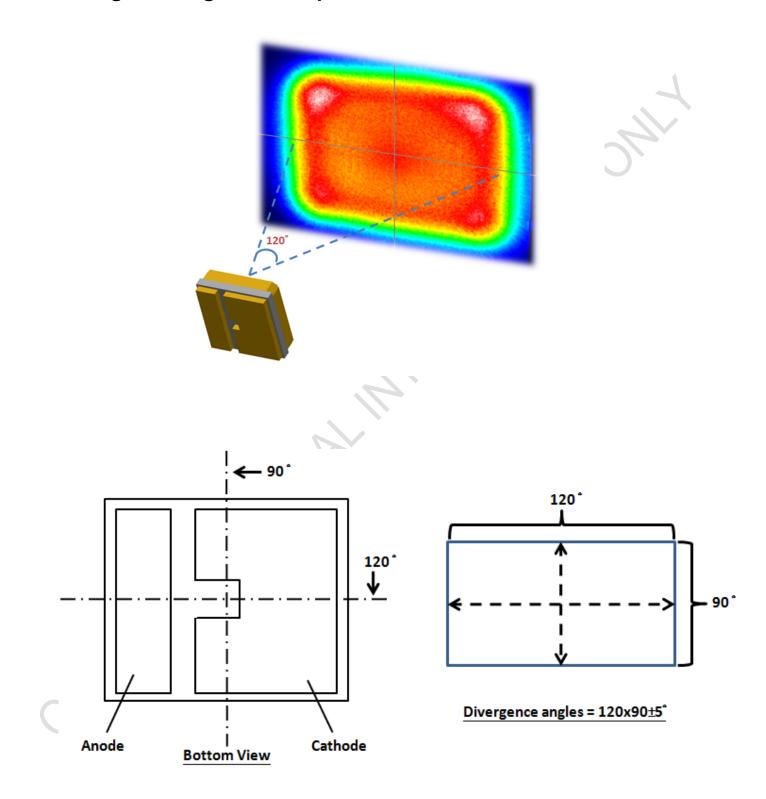
| Color | Bin Code | Typical Voltage (V) | Maximum Voltage (V) |
|--------------|----------|---------------------|---------------------|
| Infrared 940 | A1 | 2.0 | 2.3 |

• OCS maintain a tolerance of ±0.1V for Voltage measurements.

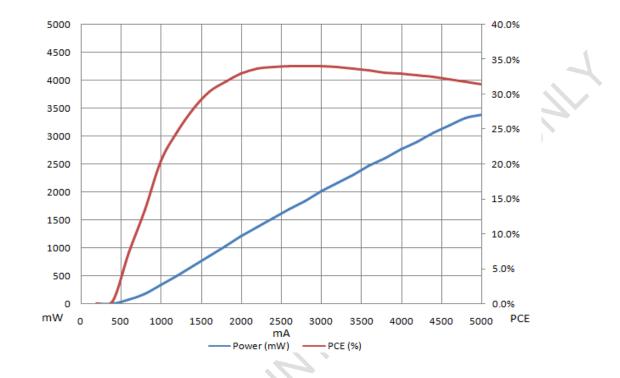
6 CONFIDER



Divergence angles corresponds to the direction

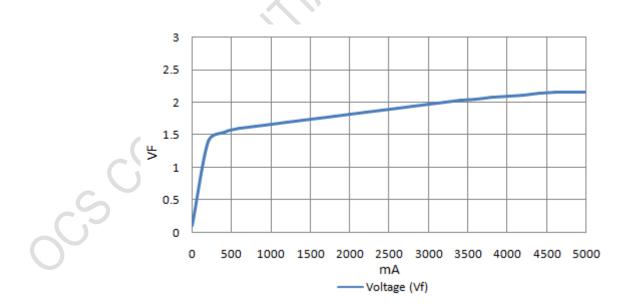






Pulse Output Power vs. Forward Current

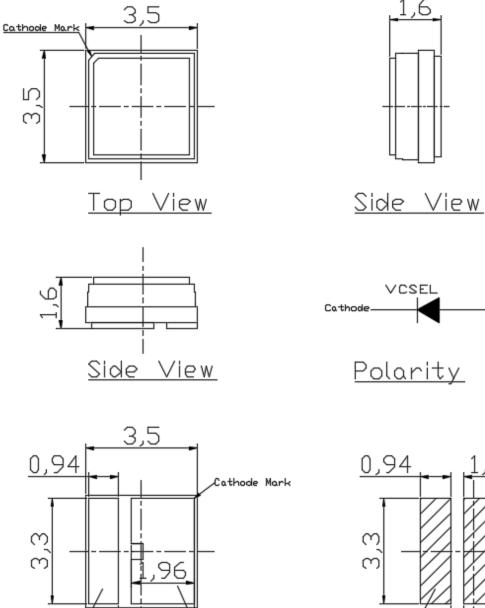
Forward Voltage vs. Forward Current



5



Outline Dimension



.96 0,94 mm 0,4 Cathode Anode

Anode

1.6

VCSEL

<u>Bottom View</u>

36

-Cathode

Recommended Solder Pad

Unit: mm Tolerance: ±0.1mm

Anode



Cleaning

- 1. If washing is required, recommend to use alcohol as a solvent.
- 2. Recommend to avoid cleaning the VCSEL by ultrasonic. If necessary, pre-test the VCSEL is necessary to confirm whether any damage occur after the process.

Precautions for use

1. Over-current-proof

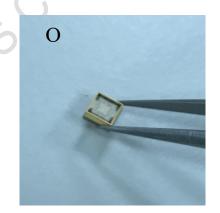
1.1 Customer must apply resistors for protection; otherwise slight voltage shift will cause big current change (Burn out will happen).

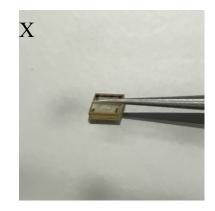
2. Storage

- 2.1 Do not open moisture proof bag before the products are ready to use.
- 2.2 Before opening the package: The VCSEL should be kept at 30°C or less and 90%RH or less.
- 2.3 After opening the package: The VCSEL floor life is 1 year under 30°C or less and 60% RH or less. If unused VCSEL remain, it should be stored in moisture proof packages.
- 2.4 If the moisture absorbent material (silica gel) has faded away or the VCSEL have exceeded the storage time, baking treatment should be performed using the following conditions. Baking treatment: 60±5°C for 24 hours.

3. Handling indications

3.1 During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound. In general, VCSEL should only be handled at the housing. This also applies to VCSEL without a diffuser, since the surface can also become scratched.

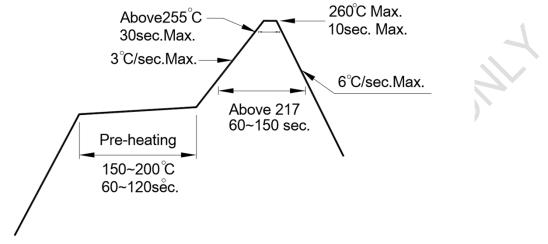






4. Soldering Condition

4.1 Pb-free solder temperature profile (JEDEC-STD-020 latest version compliant)



- 4.2 Reflow soldering should not be done more than two times.
- 4.3 When soldering, do not put stress on the VCSEL during heating.
- 4.4 After soldering, do not warp the circuit board.

5. Soldering Iron

5.1 Each terminal is to go to the tip of soldering iron temperature less than 350°C for 3 seconds within once in less than the soldering iron capacity 25W. Leave two seconds and more intervals, and do soldering of each terminal. Be careful because the damage of the product is often started at the time of the hand solder.

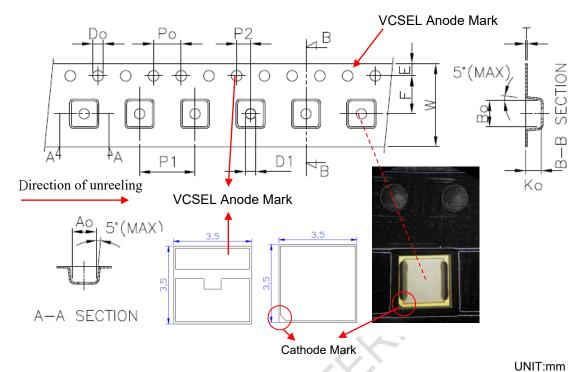
6. Repairing

6.1 Repair should not be done after the VCSEL have been soldered. When repairing is unavoidable, a double-head soldering iron should be used (as below figure). It should be confirmed beforehand whether the characteristics of the VCSEL will or will not be damaged by repairing.

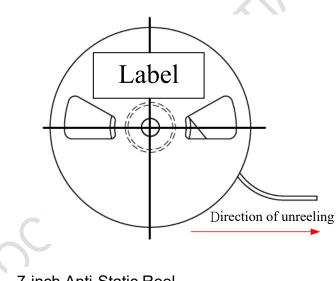




Carrier Taping



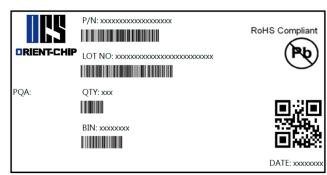
| symbol | Ao | Во | Ko | Po | P1 | P2 | Т |
|--------|-----------|-----------|-----------|-----------|-----------|-----------|-----------|
| spec | 3.80±0.10 | 3.80±0.10 | 1.85±0.10 | 4.00±0.15 | 8.00±0.10 | 2.00±0.10 | 0.23±0.05 |
| symbol | E | F | Do | D1 | W | 10Po | |
| spec | 1.75±0.10 | 5.5.±0.05 | 1.50+0.10 | 1.50±0.10 | 12.0±0.20 | 40.0±0.10 | |



7-inch Anti-Static Reel Max 500pcs/reel

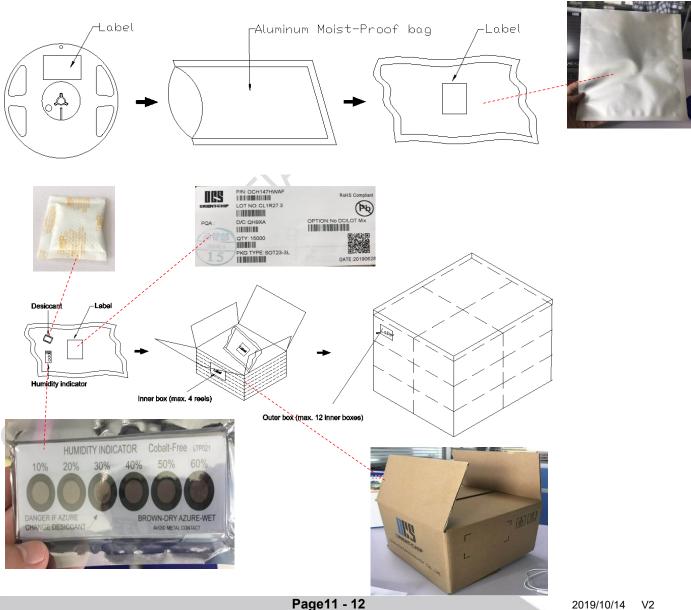


Product Labeling



- P/N: OCS Product Model Name
- LOT: Lot Number
- QTY: Q'ty
- BIN: mW/nm/Vf (ex.[A1/94/V1])
- DATE: Enter the warehouse

Moisture Resistant Packing





Modified records

| Version | Description | Issue Date |
|----------|-------------------------|------------|
| Version | Description New spec | 2019/03/20 |
| V0 V1 | Add Ith, Slope | 2019/10/05 |
| V2 | Update spec | 2019/10/14 |
| | | |