

Low On-Resistance Load Switch with Controlled Slew Rate

■ General Description

The OCP9211 features a low-Ron internal FET and an operating range of 1.2 V to 5.5 V. The switch is controlled by an on/off input which is compatible with standard CMOS GPIO. The low shut-off current allows power designs to meet standby and off-power drain specifications. On-chip discharge resistance is integrated for quick output discharge when switch is turned off. Slew-rate control prevents inrush current during switch turn-on.

The OCP9211 is available in a fully “green” compliant 0.878mm * 0.878mm WLCSP-4B Package.

■ Features

- Input Voltage Range: 1.2 V to 5.5 V
- Typical Ron
 - Ron=37mΩ at VIN=5.5V
 - Ron=47mΩ at VIN=3.3V
 - Ron=80mΩ at VIN=1.8V
 - Ron=105mΩ at VIN=1.5V
 - Ron=185mΩ at VIN=1.2V
- Slew Rate Control
- Low Quiescent Current
- Integrated Discharge Function
- 4-Bump, WLCSP 0.878 mm x 0.878 mm, 0.5mm Pitch

■ Applications

- Portable Media Players
- Cell Phones or Smart Phones
- PDAs
- Mobile Handsets
- Tablet PCs and Laptops/Net books

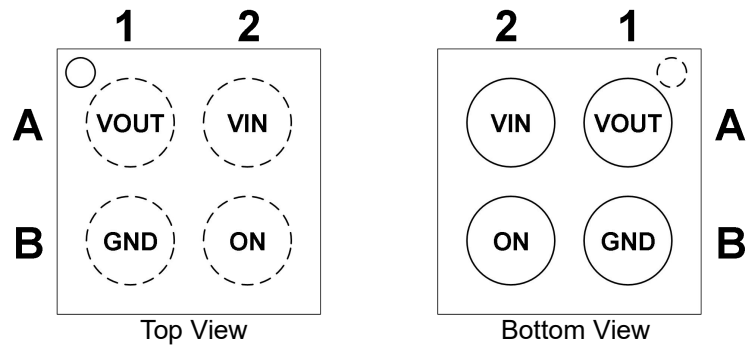
Pin Configuration


Figure 1, Pin Assignments of OCP9211

Pin Name	Pin No.	Pin Function
VOUT	A1	Switch output
VIN	A2	Switch input
GND	B1	Ground
ON	B2	ON/OFF Control, active HIGH

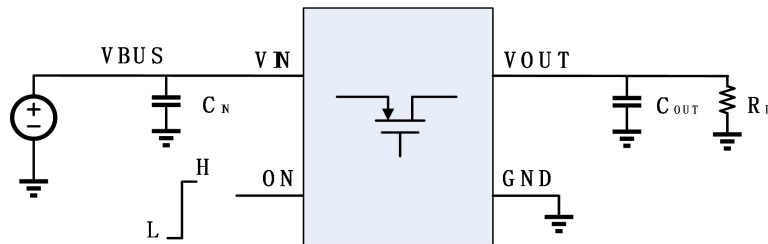
Typical Application Circuit


Figure 2 Typical Application of OCP9211

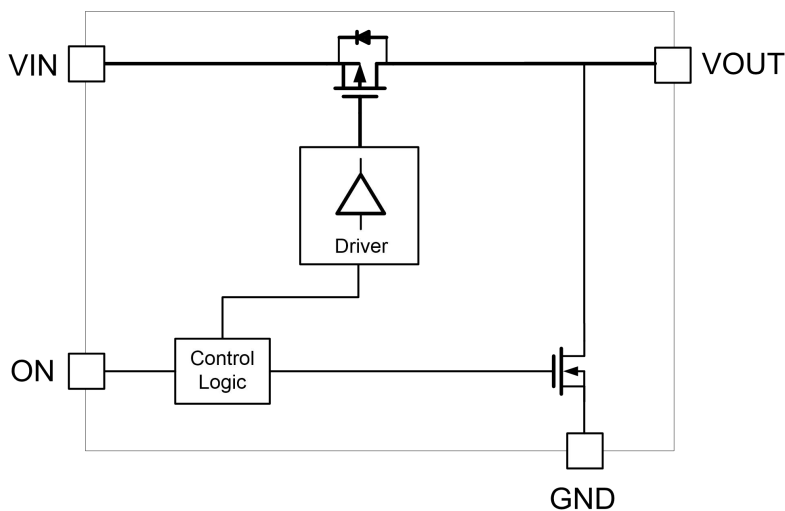
Block Diagram


Figure 3, Block Diagram of OCP9211