

300mA, Low Noise, High PSRR CMOS LDO

■ General Description

OC1203A is a low dropout, low power linear regulator which operates from 1.5V to 5.5V input voltage. OC1203A provides high power supply rejection ratio (PSRR) and delivers up to 300mA output current. OC1203A also offers low current consumption for battery operated applications.

The device is a RoHS compliant DFN10x10-4L package.

■ Applications

- Smart phones, Cell phone, PDAs
- Bluetooth, Wireless handsets
- Portable equipment

■ Features

- Input Voltage Range: 1.5V to 5.5V
- Output Voltage Range: 0.9V to 3.6V
- Output Current: 300mA
- Low Quiescent Current: 40μA(TYP)
- Shut Down Current: <1μA
- Auto-Discharge function
- Available in DFN10x10-4L package
- -40°C to +85°C Operating Temperature Range

■ Pin Configuration

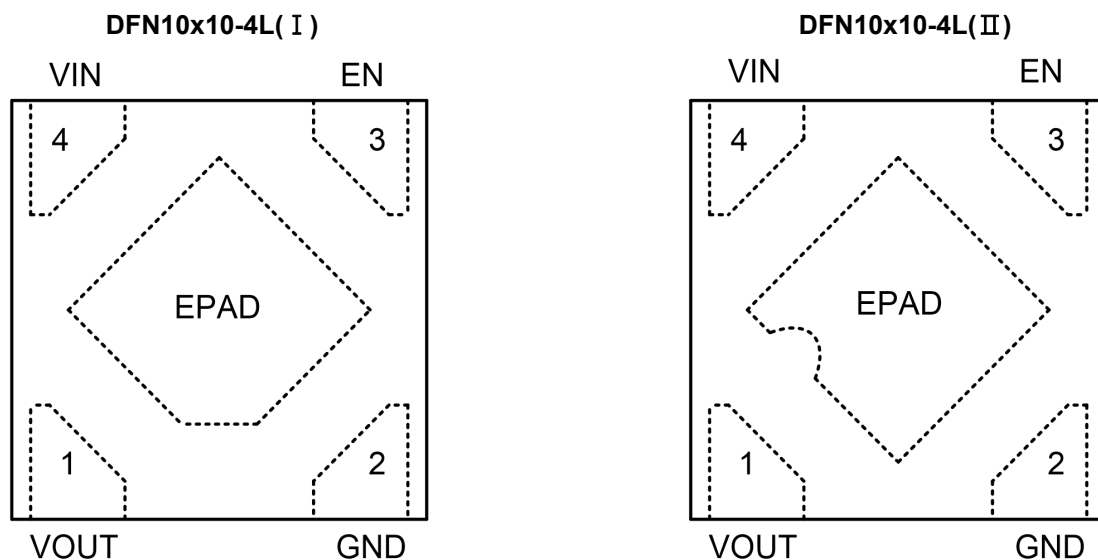


Figure 1, Pin Assignments of OC1203A (Top View)

Pin No.	Pin Name	Pin Function
1	VOUT	Regulator Output Pin. Bypass a 1μF capacitor to ground
2	GND	Ground
3	EN	Enable control pin, active high. When EN pin is floating, it will be shutdown mode.
4	VIN	Regulator Input Pin. 1μF decouple capacitor is needed.
Exposed PAD	-	The exposed pad should be connected to a large ground plane to maximize thermal performance.

■ Typical Application Circuit

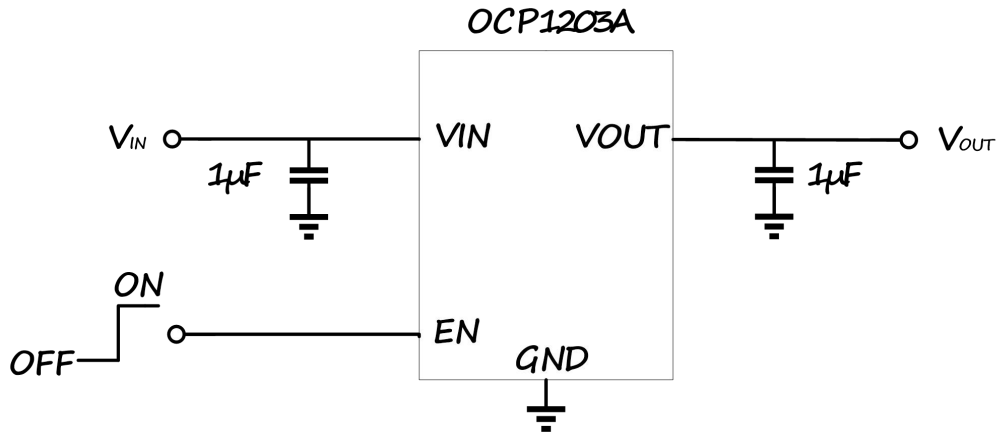


Figure 2, Typical Application Block diagram of OCP1203A

■ Block Diagram

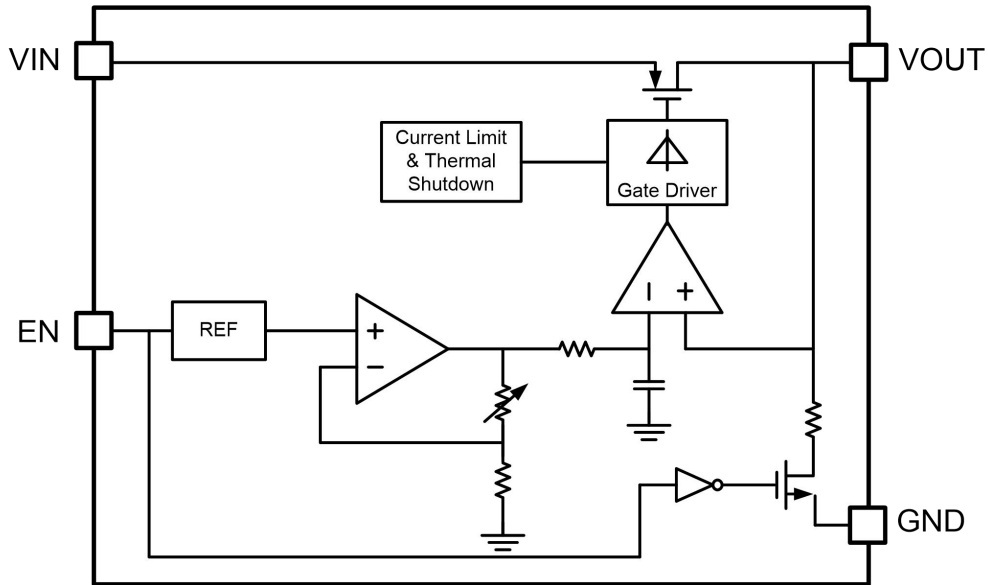


Figure 3, Block diagram of OCP1203A