300mA, Low Noise, High PSRR CMOS LDO

■ General Description

OCP1303 is a low dropout, low power linear regulator which operates from 2.2V to 5.5V input voltage.
OCP1303 provides high power supply rejection ratio (PSRR) and delivers up to 300mA output current.
OCP1303 also offers low current consumption for battery operated applications.
The device is a RoHS compliant DFN1010-4L package.

■ Features

- Input Voltage Range: 2.2V to 5.5V
- Output Voltage Range: 1.0V to 3.8V
- Output Current: 300mA
- Low Quiescent Current: 40μA(Typ.)
- Shut Down Current: <1μA
- Auto-Discharge function
- Available in DFN package
- -40℃ to +85℃ Operating Temperature Range

■ Applications

- Smart phones, Cell phone, PDAs
- Bluetooth, wireless handsets
- Portable equipment

■ Pin Configuration

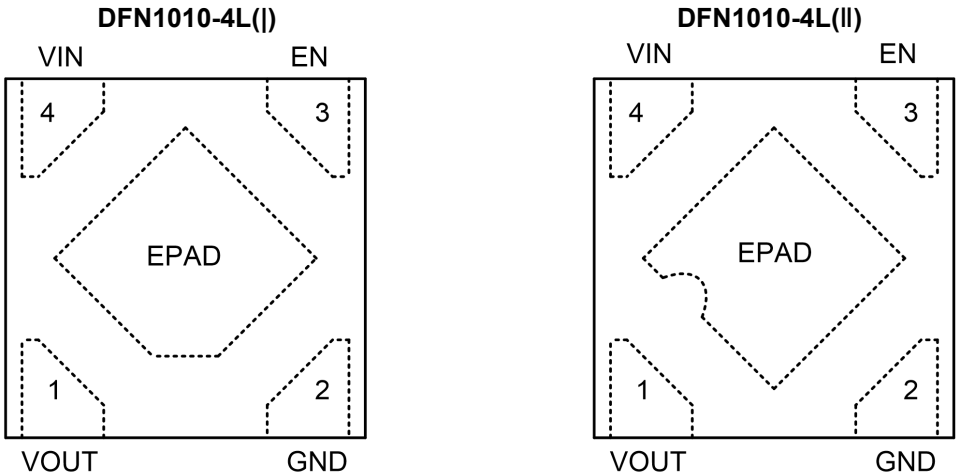

Figure 1, Pin Assignments of OCP1303 (Top View)

Pin No.	Pin Name	Pin Function
1	VOUT	Regulator Output Pin. Bypass a 1μF capacitor to ground
2	GND	Ground
3	EN	Enable control pin, active high. When EN pin is floating, it will be shutdown mode.
4	VIN	Regulator Input Pin. 1μF decouple capacitor is needed.
Exposed PAD	-	The exposed pad should be connected to a large ground plane to maximize thermal performance.

Typical Application Circuit

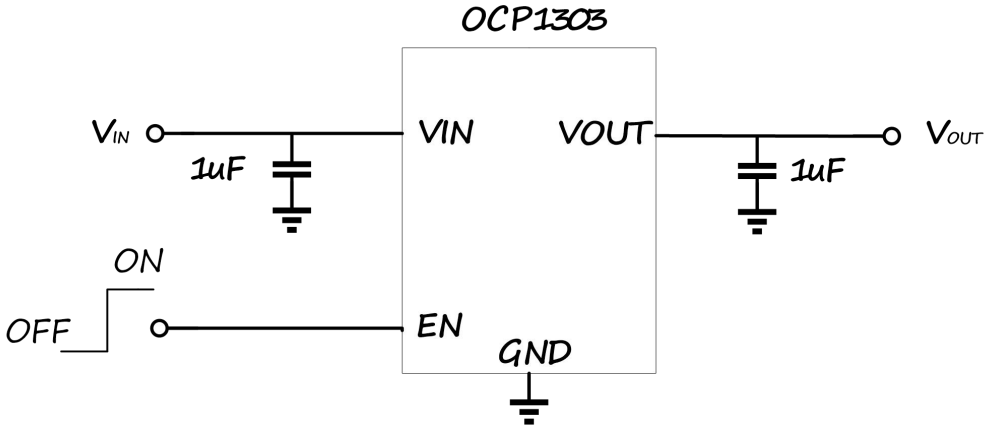

Figure 2, Typical Application Circuit of OCP1303

Block Diagram

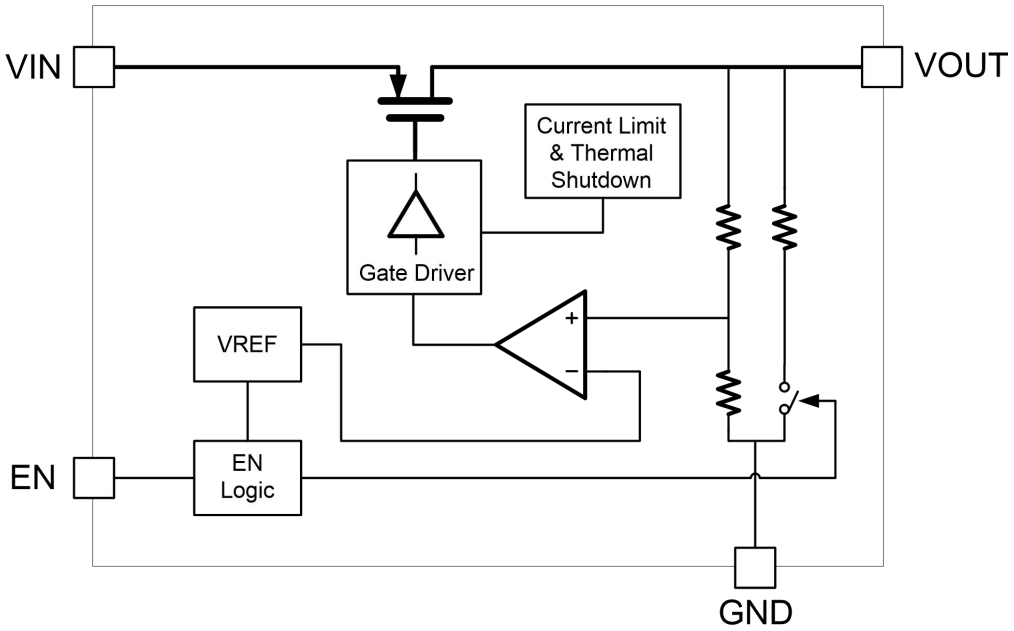

Figure 3, Block diagram of OCP1303