

## General Description

The OCH1900 is small, versatile linear Hall effect devices which are operated by the magnetic field from a permanent magnet or an electromagnet. They are optimized to accurately provide a voltage output that is proportional to an applied magnetic field. These devices have a quiescent output voltage that is 50% of the supply voltage.

The Hall-effect integrated circuit included in each device includes a Hall sensing element, a linear amplifier, and a CMOS Class AB output structure. Integrating the Hall sensing element and the amplifier on a single chip minimizes many of the problems normally associated with low voltage level analog signals.

High precision in output levels is obtained by internal gain and offset trim adjustments made at end-of-line during the manufacturing process.

The integrated circuitry provides increased temperature stability and sensitivity, for both linear target motion and rotational motion. These linear position sensors have an operating temperature range of -40°C to +150°C, appropriate for industrial environments. They respond to either positive or negative gauss, monitoring either or both magnetic poles. The quad Hall sensing element minimizes the effects of mechanical or thermal stress on the output. The positive temperature coefficient of the sensitivity helps compensate for the negative temperature coefficients of low-cost magnets, providing a robust design over a wide temperature range.

The OCH1900 is available in small 3-pin SOT23-3L、SIP-3L and DFN1616-3L package, and is rated over the -40°C to +150°C. These packages are available in a lead (Pb) free version.

## Features

- Supply Voltage Range: 3.0V ~ 5.5V
- Fast Power-on Time
- Power Consumption of 8mA at 5V Supply Voltage
- Single Current Sinking or Current Sourcing Output
- Linear Output For Circuit Design Flexibility
- Ratio metric Output for A/D Interface
- Wide Sensible Magnetic Field Range on Different Supplied Voltage  $\pm 1466$  Gauss on 5V Supplied Voltage
- Rail to Rail Operation Provides More Useable Signal For Higher Accuracy
- Temperature Stable Quiescent Output Voltage
- Quad Hall Sensing Element For Stable Output
- Low Noise Output
- Responds to Either Positive or Negative Gauss
- Robust EMC Protection
- Temperature Range of -40°C to +150°C
- Miniature Package SOT23-3L、SIP-3L and DFN1616-3L

## Applications

- Current Sensing
- Motor Control
- Linear Position Sensing
- Magnetic Code Reading
- Rotary Position Sensing
- Ferrous Metal Detector
- Vibration Sensing
- Liquid Level Sensing
- Weight Sensing

## Pin Configuration

### SOT-23-3L & SIP-3L (Top View) & DFN1616-3L

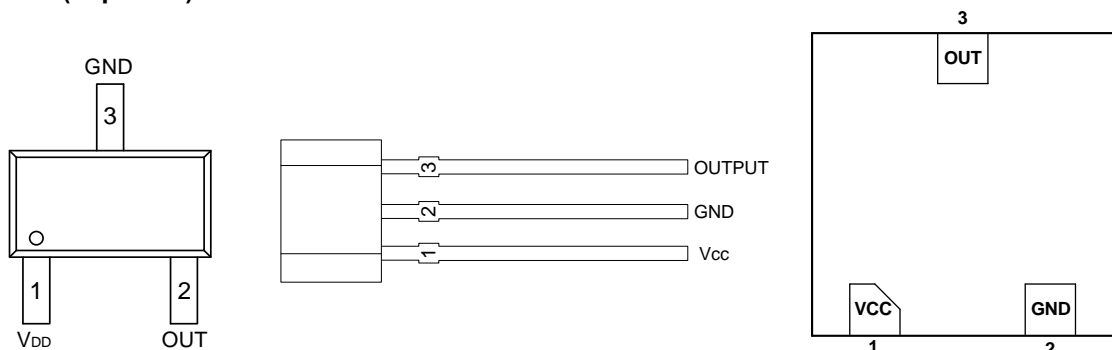


Figure 1, Pin Assignments of OCH1900

Pin Name	Pin Function			IO	Description
	SOT23-3L	SIP-3L	DFN1616-3L		
VDD	1	1	1	P	Power Supply Pin.
GND	3	2	2	P	Ground Pin.
OUTPUT	2	3	3	O	Linear Voltage Output Pin.

## ■ Typical Application Circuit

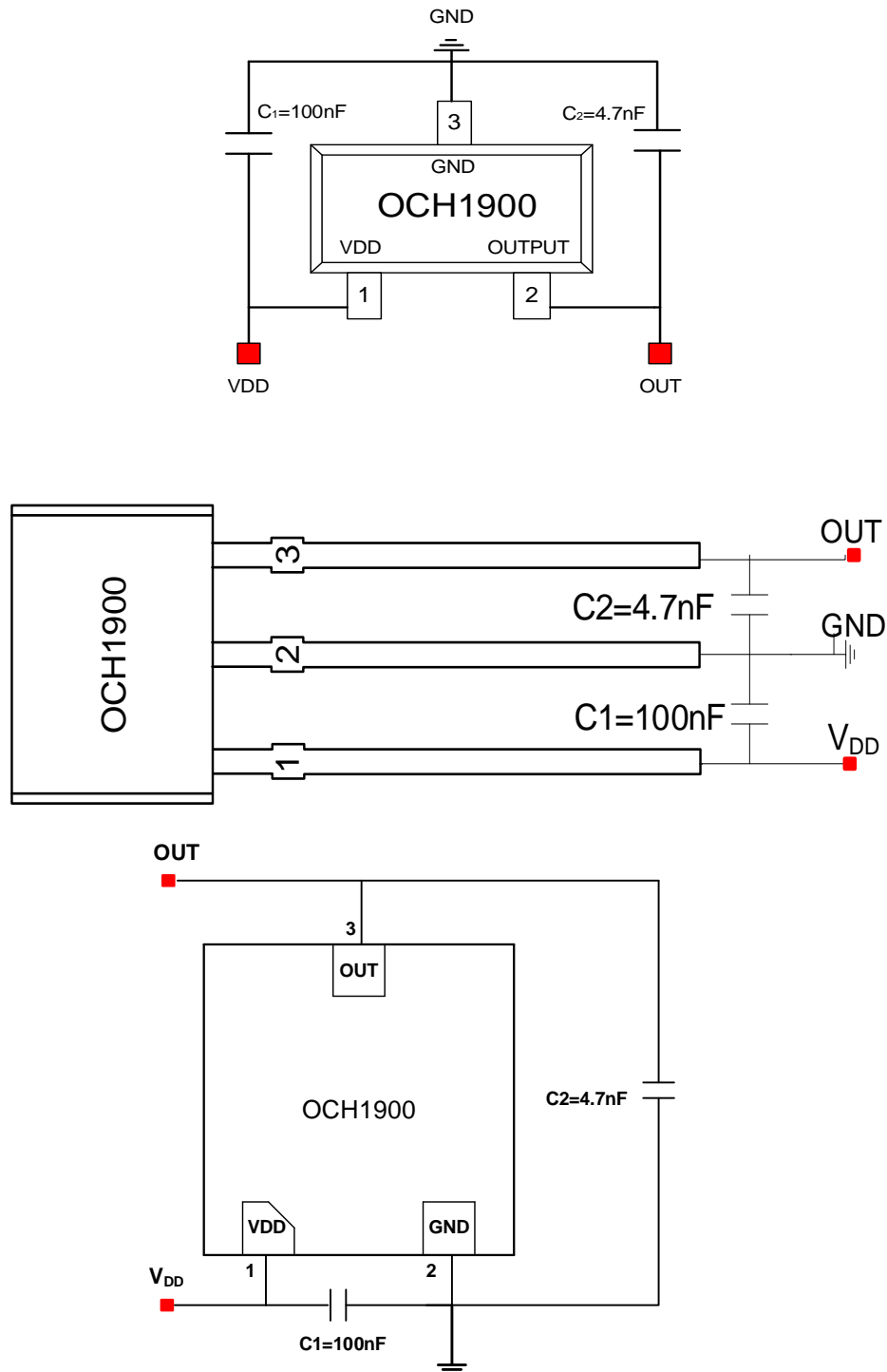


Figure 2, Typical Application Circuit



## ■ Transfer Characteristics (Vcc=5V)

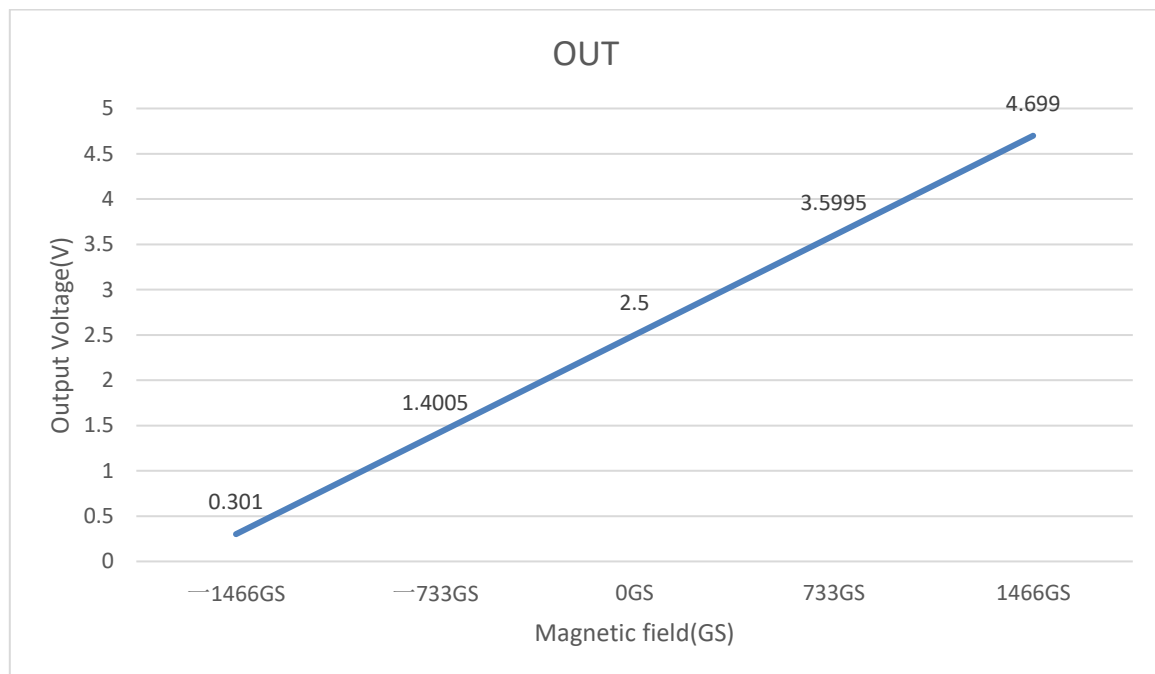
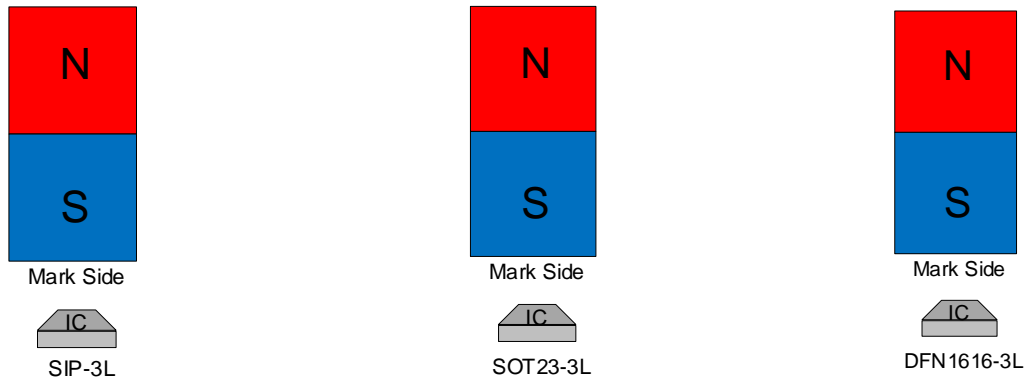


Figure 3, The Transfer Characteristics of OCH1900

Note: C1 is for power stabilization and to strengthen the noise immunity, the recommended capacitance is 100nF. C2 is a stable output. In order to enhance anti-noise capability, it is recommended that the capacitor be set to 4.7nF.

## ■ Ordering Information

Part Number	Sensitivity (Typ.)	Package Type	Package Qty	Temperature	Eco Plan	Lead
OCH1900WAE	1.5mV/Gauss@5V	SOT23-3L	7-in reel 3000pcs/reel	-40~150℃	Green	Cu
	1.0mV/Gauss@3.3V					
OCH1900ME	1.5mV/Gauss@5V	SIP-3L	Bulk 1000pcs/bag	-40~150℃	Green	Cu
	1.0mV/Gauss@3.3V					
OCH1900V3AD	1.5mV/Gauss@5V	DFN1616-3L	7-in reel 3000pcs/reel	-40~150℃	Green	Cu
	1.0mV/Gauss@3.3V					



## ■ Block Diagram

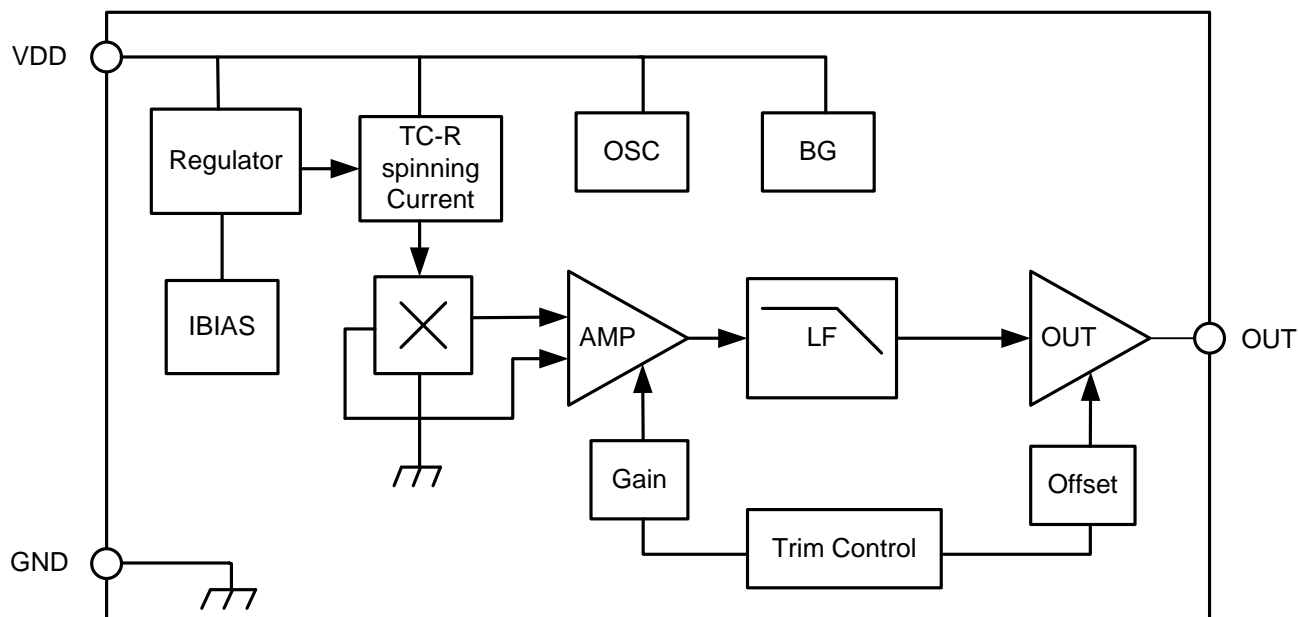


Figure 4, Block Diagram of OCH1900

## ■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
V <sub>DD</sub> to GND	V <sub>DD</sub>	-0.3 to 25	V
Output to GND	V <sub>OUTPUT</sub>	-0.3 to V <sub>DD</sub>	V
Output Current	I <sub>OUT</sub>	±5	mA
Power Dissipation (for SOT23-3L)	P <sub>D</sub>	260	mW
Power Dissipation (for SIP-3L)	P <sub>D</sub>	400	mW
Power Dissipation (for DFN1616-3L)	P <sub>D</sub>	550	mW
Lead Temperature (Soldering, 10 Sec.)	T <sub>LT</sub>	260	°C
Storage Temperature Range	T <sub>S</sub>	-55 to +150	°C
Maximum Operating Junction Temperature	T <sub>J</sub>	165	°C
ESD (HBM)	ESD	8K	V

## ■ Recommended Operating Conditions

Parameter	Symbol	Conditions	Rating	Unit
Supply Voltage	V <sub>DD</sub>	Operating	3.0 ~ 5.5	V
Output Voltage	V <sub>OUT</sub>	Operating	0.3 ~ (V <sub>DD</sub> -0.3)	V
Operating Temperature Range	T <sub>OP</sub>	Operating	-40 ~ +150	°C



## ■ Electrical Characteristics

(Unless otherwise noted, typical values are at  $T_A=25^{\circ}\text{C}$ ,  $V_{DD}=5.0\text{V}$ )

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
<b>General Supply</b>						
$V_{DD}$	Power supply	-	3.0	-	5.5	V
$I_Q$	Quiescent Current	-	-	8.0	10.0	mA
$T_{PO}$	Power On Time <sup>1</sup>	-	-	30	50	μs
<b>Gauss Sensitivity</b>						
$A_{SEN}$	Sensitivity@5V	-	1.395	1.50	1.605	mV/Gauss
	Sensitivity@3.3V	-	0.90	1.00	1.10	mV/Gauss
	Temperature Coefficient <sup>1</sup>	-	-10	-	+10	%
MGR	Measurable Gauss Range@5V	-	-	±1466	-	Gauss
	Measurable Gauss Range@3.3V	-	-	±1350	-	Gauss
BW	Bandwidth <sup>1</sup>	-	-	50	-	KHz
Chopping Frequency	$f_c$ <sup>1</sup>	-	-	400	-	KHz
<b>Ratiometry</b>						
$\Delta V_{OQ(\Delta V)}$	Output Voltage Error with Respect to $\Delta V_{DD}$ <sup>1</sup>	B=0 Gauss	-1.5	-	1.5	%
$\Delta S_{EN(\Delta V)}$	Magnetic Sensitivity Error with Respect to $\Delta V_{DD}$ <sup>1</sup>	-	-1.5	-	1.5	%
<b>Output Stage</b>						
$V_{OQ}$	0 Gauss Output Voltage @5V	-	2.475	2.50	2.525	V
	0 Gauss Output Voltage @3.3V	-	1.62	1.65	1.68	V
	Temperature Coefficient <sup>1</sup>	-	-1.5	-	1.5	%
Line	Output Voltage Linearity <sup>1</sup>	-	-1.5	-	1.5	%
Sym.	Symmetry <sup>1</sup>	-	-1.5	-	1.5	%
$V_{OH}$	Output Maximum Voltage	$I_{OH}=1.5\text{mA}$	-	$V_{DD}-0.2$	-	V
$V_{OL}$	Output Minimum Voltage	$I_{OL}=-1.5\text{mA}$	-	0.2	-	V
$R_{OUT}$	Output Resistance	$I_{out} \leq \pm 1.5\text{mA}$ $V_{OUT}=2.5\text{V}$	-	2	4	Ω
$C_L$	Output Loading Resistance	$I_{out} \leq \pm 1.5\text{mA}$ , Output to GND	-	-	10	nF
$V_{NOISE}$	Wide Band Output Noise <sup>1</sup>	-	-	1.1	-	mVRms
$I_{SOURCE}$	Source Current	B=0, $V_{out}=\text{gnd}$	-	-	1.5	mA
$I_{SINK}$	Sink Current	B=0, $V_{out}=V_{CC}$	-1.5	-	-	mA

1) Guaranteed by design.

## ■ Application Information

### Quiescent Voltage Output:

In the quiescent state (no magnetic field), the output equals about 2.5V over the operating voltage range and the operating temperature range. Due to internal component tolerances and thermal considerations, there is a tolerance on the quiescent voltage output both as a function of supply voltage and as a function of ambient temperature. For purposes of specification, the quiescent voltage output as a function of temperature is defined in terms of magnetic flux density, B, as:

$$\Delta V_{OQ(\Delta T)} = \frac{V_{OQ(TA)} - V_{OQ(25^{\circ}\text{C})}}{V_{OQ(25^{\circ}\text{C})}} * 100\%$$

This calculation yields the device's equivalent accuracy, over the operating temperature range, in gauss (G).



### Sensitivity:

The presence of a south-pole magnetic field perpendicular to the package face (the branded surface) increases the output voltage from its quiescent value toward the supply voltage rail by an amount proportional to the magnetic field applied. Conversely, the application of a north pole will decrease the output voltage from its quiescent value. This proportionality is specified as the sensitivity of the device and is defined as:

$$A_{SEN} = \frac{V_{OUT(-B)} - V_{OUT(+B)}}{2B}$$

The stability of sensitivity as a function of temperature is defined as:

$$\Delta A_{SEN(\Delta T)} = \frac{A_{SEN(TA)} - A_{SEN(25^\circ C)}}{A_{SEN(25^\circ C)}} * 100\%$$

### Ratio-metric:

The OCH1900 family features a ratio-metric output. The quiescent voltage output and sensitivity are proportional to the supply voltage (ratio-metric). The percent ratio-metric change in the quiescent voltage output is defined as:

$$\Delta V_{OQ(\Delta V)} = \frac{V_{OQ(VDD)}/V_{OQ(5V)}}{V_{DD}/5V} * 100\%$$

and the percent ratio-metric change in sensitivity is defined as:

$$\Delta A_{SEN(\Delta V)} = \frac{A_{SEN(VDD)}/A_{SEN(5V)}}{V_{DD}/5V} * 100\%$$

### Linearity and Symmetry:

The on-chip output stage is designed to provide a linear output with a supply voltage of 5V. Although application of very high magnetic fields will not damage these devices, it will force the output into a non-linear region. Linearity in percent is measured and defined as:

$$Linear+ = \frac{V_{OUT(+B)} - V_{OQ}}{2(V_{OUT(+B/2)} - V_{OQ})} * 100\%$$

$$Linear- = \frac{V_{OUT(-B)} - V_{OQ}}{2(V_{OUT(-B/2)} - V_{OQ})} * 100\%$$

and output symmetry as:

$$Sym = \frac{V_{OUT(+B)} - V_{OQ}}{V_{OQ} - V_{OUT(-B)}} * 100\%$$

### Thermal Considerations:

For continuous operation, do not exceed the maximum operation junction temperature 165°C. The maximum power dissipation depends on the thermal resistance of IC package, PCB layout, the rate of surroundings airflow and temperature difference between junction temperatures to ambient temperature. The maximum power dissipation can be calculated by following formula:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_A}{\theta_{JA}}$$

The value of junction to case thermal resistance  $\theta_{JC}$  is popular for users. This thermal parameter is convenient for users to estimate the internal junction operated temperature of packages while IC operating. It's independent of PCB layout, the surroundings airflow effects and temperature difference between junction to ambient. The operated junction temperature can be calculated by following formula:

$$T_J = T_C * P_D * \theta_{JC}$$

Where  $T_C$  is the package case temperature measured by thermal sensor,  $P_D$  is the power dissipation defined by user's function and the  $\theta_{JC}$  is the junction to case thermal resistance provided by IC manufacturer. Therefore, it's easy to estimate the junction temperature by any condition.



## ■ Hall Sensor Location

The Fig5 is hall sensor location, where marks the IC number.

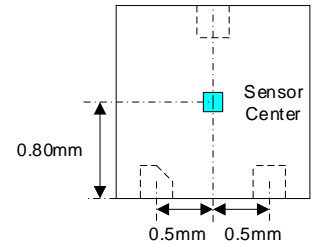
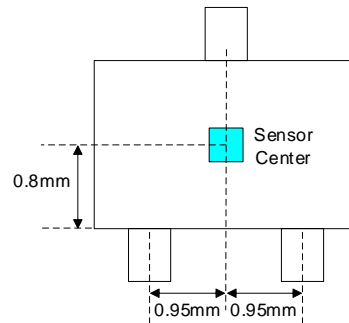
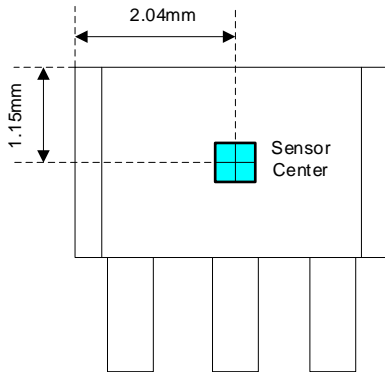
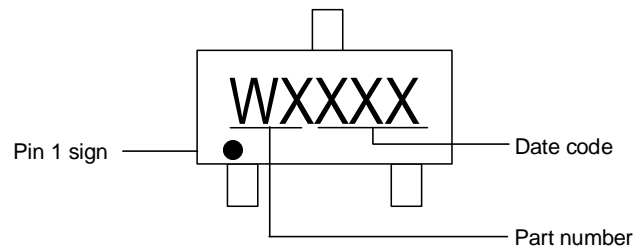


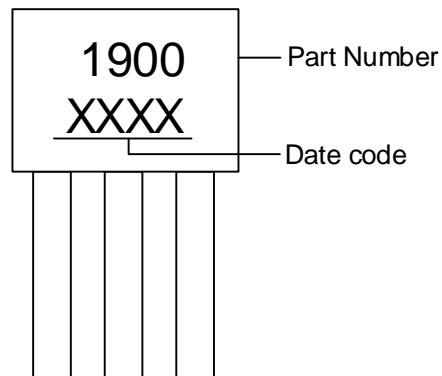
Fig5, OCH1900 Hall Sensor Location

## ■ Marking Information

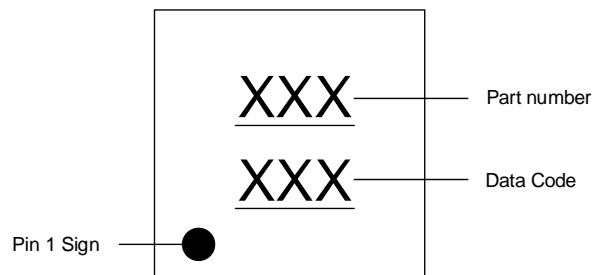
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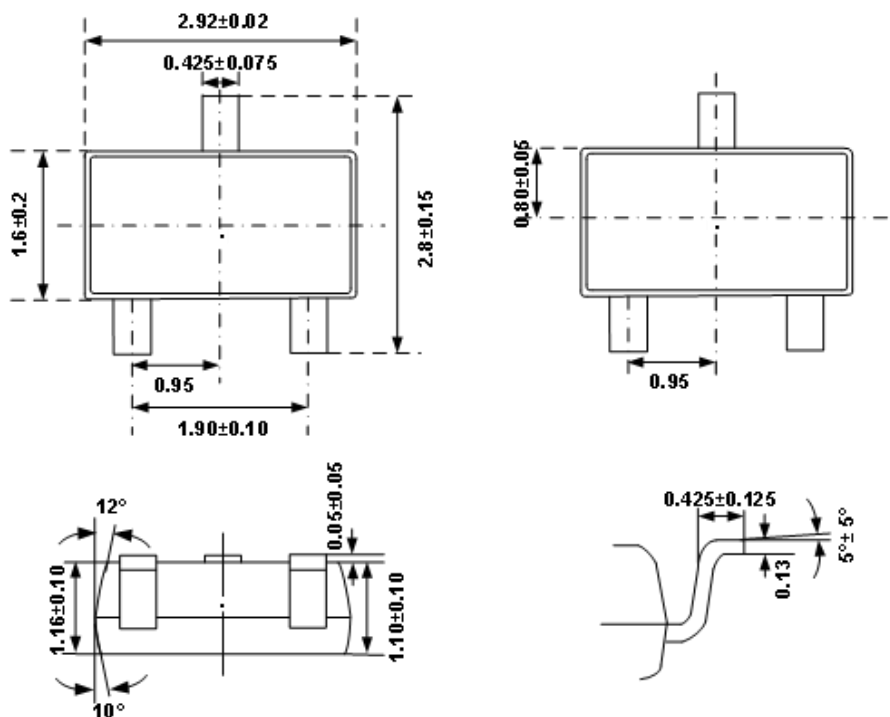


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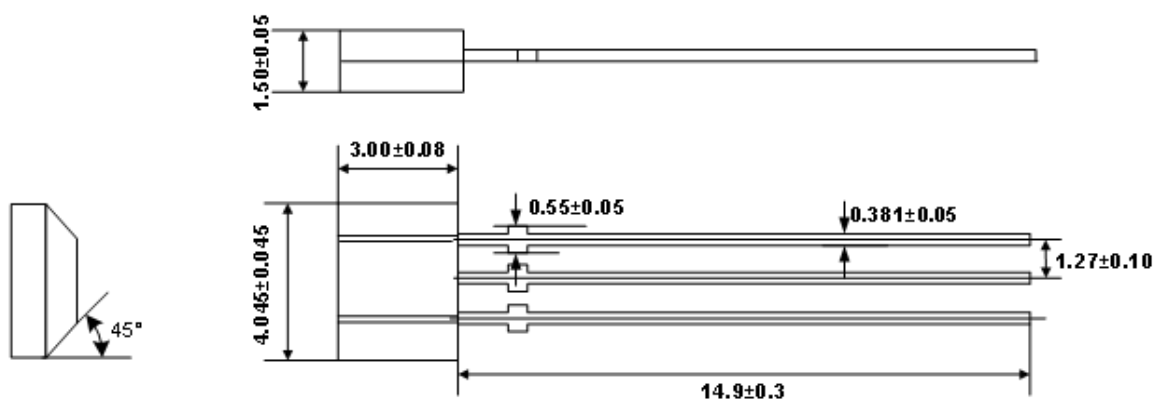


## ■ Package Information

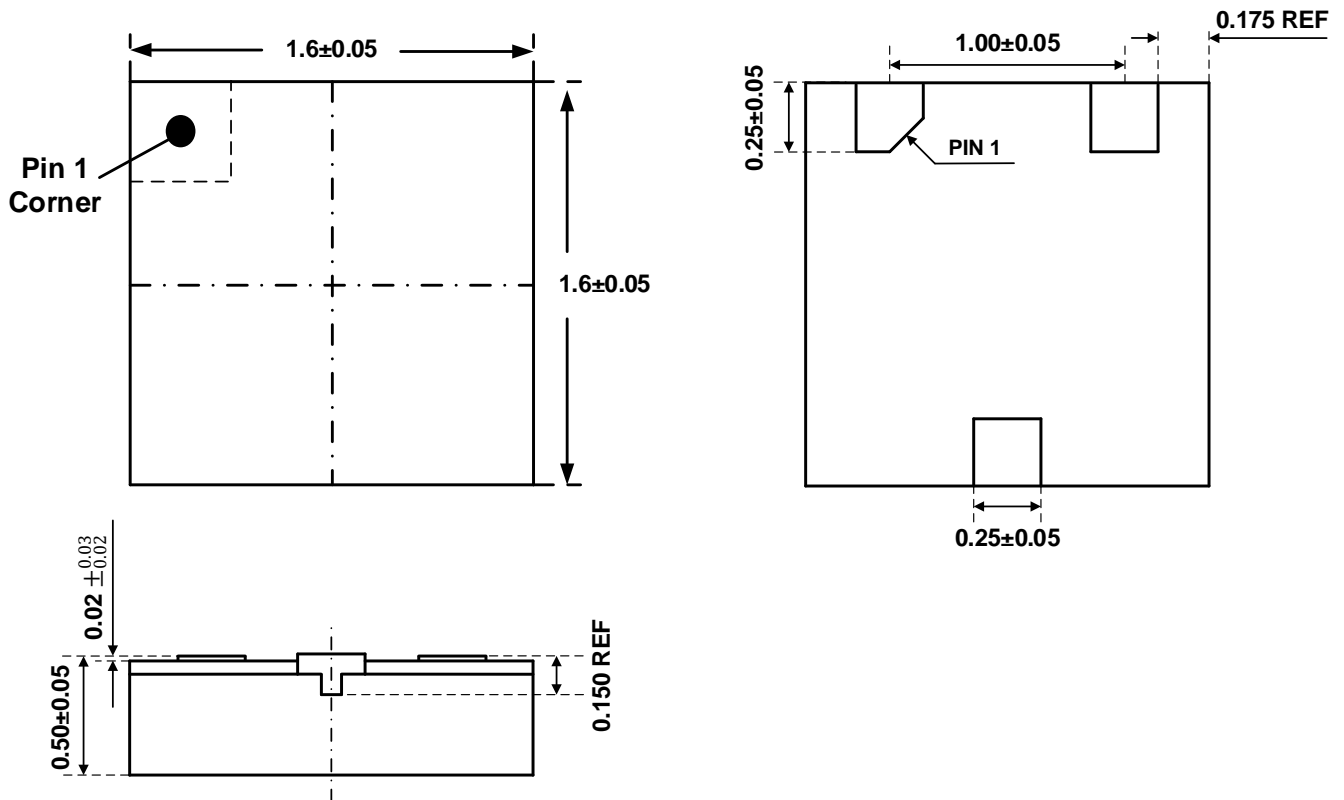
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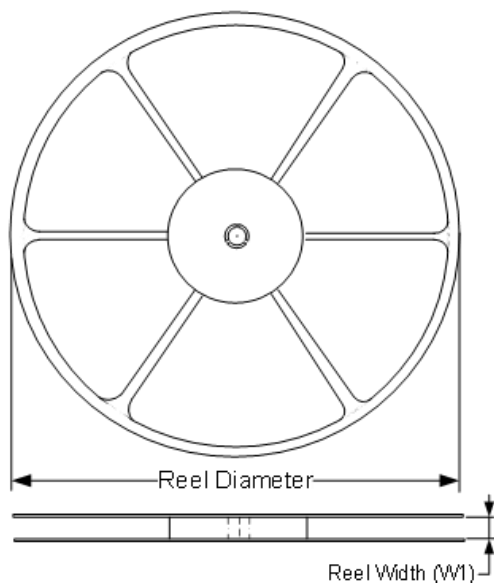
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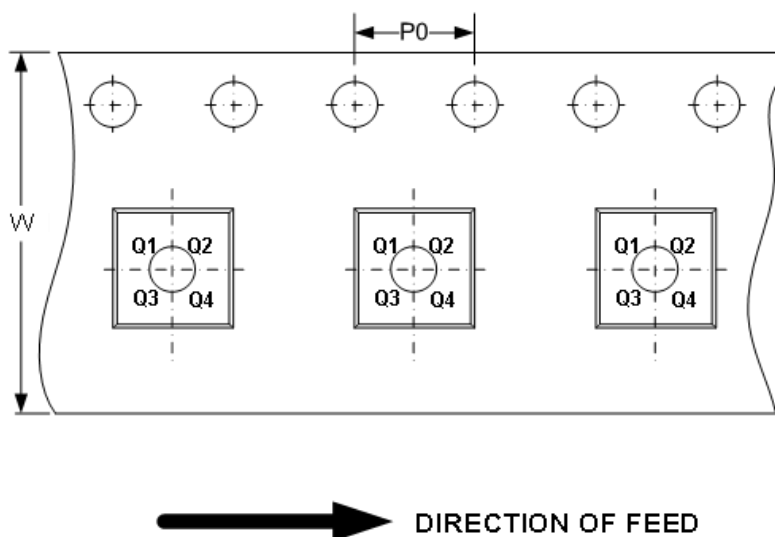
## ■ Packing Information

1), SOT23-3L & DFN1616-3L

### REEL DIMENSIONS



### TAPE DIMENSIONS



Package type	SPQ (PCS)	Reel Diameter (mm)	Reel Width W1(mm)	W (mm)	P0 (mm)	MSL	Pin 1 Quadrant
SOT23-3L	3000	180	8.6	8.0	4.0	Level-3	Q3
DFN1616-3L	3000	180	8.6	8.0	4.0	Level-3	Q1

Note: Carrier Tape Dimension, Reel Size and Packing Minimum

2), SIP-3L

1. Packing type: Bulk
2. Packing minimum: 1000pcs



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